

PTO/SB/08A (08-03)

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		Application Number	10/666,220
		Filing Date	9/19/2003
		First Named Inventor	Cho
		Art Unit	2818
		Examiner Name	Tran, Mai Huong C
Sheet 1 of 2	Attorney Docket Number	C002-D-2	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
MC		US- 3,943,549	3-9-1976	Jaecklin et al.	
MC		US- 4,165,517	8-21-1979	Temple et al.	
MC		US- 4,281,336	7-28-1981	Sommer et al.	
MC		US- 4,323,793	4-6-1982	Schutten et al.	
MC		US- 4,646,121	2-24-1987	Ogura	
MC		US- 4,829,357	5-9-1989	Kasahara	
MC		US- 4,864,168	9-5-1989	Kasahara et al.	
MC		US- 4,982,258	1-1-1991	Baliga	
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MC		US- 5,463,344	10-31-1995	Temple	
MC		US- 4,956,688	09-1990	Honma et al.	
MC		US- 6,229,161	5-8-2001	Nemati et al.	
MC		US- 6,104,045	8-15-2000	Forbes et al.	
MC		US- 6,225,165	5-1-2001	Noble, et al.	
		US-			
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		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)			

Examiner Signature		Date Considered	03/22/05
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Sheet 2	of 2	Attorney Docket Number	C002-D-2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
MH		Nemati, Farid and Plummer, James, D., A Novel High Density, Low Voltage SRAM Cell with a Vertical NDR Device, VLSI Technology Technical Digest, 6/1998	
MH		Nemati, Farid and Plummer, James, D., A Novel Thyristor-based SRAM Cell (T-RAM) for High-Speed, Low-Voltage, Giga-scale Memories, International Electron Device Meeting Technical Digest, 1999	
MH		Gribnikov, Z.S., Korobov, V.A., and Mitin, V.V., The Tunnel Diode as a Thyristor Emitter, Solid-State Electronics, Vol. 42, No. 9, 1998, pp. 1761-1763	

Examiner Signature		Date Considered	3-22-05
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